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Konyha, L.; Enyedi, B.; Tran, S.M.; Fazekas, K.;

Video/Image Processing and Multimedia Communications, 2003. 4th EURASIP Conference focused on , Volume: 1 , 2-5 July 2003

Pages:175 - 180 vol.1

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Digital Audio Broadcasting

... flash converters with <250picosecond windows, one to **detect** each **phase** ... signal to lock on to the **phase** clock of ... **ECC frame** size is 16 kilobits (8 kilosymbols, 8 ...

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... period lasts 32T, which makes it much easier to **detect**. ... the pre-pits more error prone than **phase** modulation, but ... which gives at least one sync **frame** less to ...

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... High Speed Search User Data Area Previous Group written Total Group written Previous Group read Total Group read Total Rewritten **frame** Total 3rd **ECC** count Load ...

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... Response **Phase** Bus Transaction Initiator Termination TRDY# IRDY# **FRAME#** BUS CMD C ... bit correction on all phases Supports nibble **detect** and **phase** error **detect** ...

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... The mean screen luminance was 100 cd/m² and the **frame** rate was ... F IGURE 3. Comparison of the contrast needed to **detect** and to discriminate the **phase** (0° vs ...

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... SDAT 32 26 SBCK S0S1 EFM **PHASE** DETECTOR 23BIT ... SUBCODE-Q REGISTER DIGITAL PLL **FRAME** SYNC DETECTOR ... MODE SELECTOR DIGITAL OUTPUT 16K SRAM **ECC** INTERPOLATOR DIGITAL ...

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... all stations going to a backoff **phase** before transmission ... In order to **detect** a collision, the entire ... **Frame** Space) • EIFS (Extended Inter **Frame** Space) SIFS and ...

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... On means 1, Off means 0 • Similar: frequency or **phase** modulation ... byte stuffing • Encoding • Add implied 0 to end of **frame** • Each 0 ... Can **detect** all burst ...

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